[METHOD AND APPARATUS FOR INITIAL-IZING SRAM DEVICE DURING POWER-UP]

Abstract

A method for initializing a static random access memory (SRAM) device during power-up includes clamping one of a pair of bit-lines of the SRAM device to a logic low potential while allowing the other of the pair of bitlines to be coupled to a charging logic high potential. An SRAM storage cell within the SRAM device is forced to a stable state by selectively allowing a wordline potential of a wordline associated with the SRAM storage cell to follow the charging logic high potential, thereby coupling the SRAM storage cell to the pair of bitlines.